

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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| Application Serial No.   | 09/653,149              |
| Confirmation No.   | 4634                    |
| Filing Date  | August 31, 2000         |
| Inventor   | Garo J. Derderian       |
| Assignee   | Micron Technology, Inc. |
| Group Art Unit   | 2818                    |
| Examiner   | T. Le                   |
| Attorney's Docket No   | MI22-1330               |
| Customer Number  |                         |
| Title: Capacitor Fabrication Methods Including Forming a Conductive Layer (as amended) |                         |

## **RESPONSE TO MAY 15, 2003 OFFICE ACTION**

To:

Commissioner for Patents

P.O. Box 1450 Art Unit 2818

Alexandria, VA 22313-1450

From:

James E. Lake (Tel. 509-624-4276; Fax 509-838-3424)

Wells St. John P.S.

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## **AMENDMENTS**